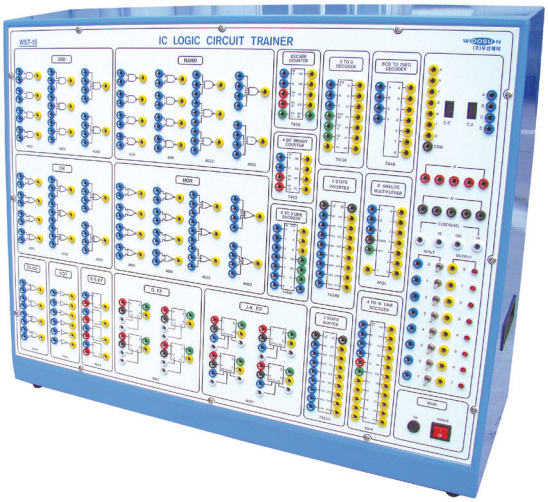
Logic Circuit Trainer



[WST-15]

[WST-16]

Experiment

- AND Gate / OR Gate / NOT Gate / NAND Gate / NOR Gate / EX-OR Gate
- RS Flip-Flop / D Flip-Flop / JK Flip-Flop
- DECADE Counter / 3 TO 8 DECODER / BCD TO 7 Segment DECODER
- 4 BIT BINARY Counter / 8 TO 3 Encoder / 8 Analog Multiplexor
- 4 TO 16 Decode / 3 State Inverter and Buffer Experiment
- Switching Gate with Inverter / EX-OR by means of NAND gate
- R-S Flip-Flop by means of NAND gate / NOT circuit by means of NAND and NOR
- 3 Bit Counter Circuit / Johnson Counter / UP Counter and Down Counter
- De-Morgan's Theorem / Half-Adder and Full-Adder

Specification

- Main Voltage : 1 Phase 220V ~ 240V / 50/60Hz
- Input Switch Block
- Clock Block
  - Output : 1Hz / 10Hz / 100Hz / 1KHz
- 7-Segment Block
- NAND Gate Block
- NOR Gate Block
- NOT-Gate Block
- D Flip-Flop Block
- Decade Counter Block
- BCD To 7-Seg Decoder Block
- 8 To 3 Line Encoder Block
- 8 Analog Multiplexer Block
- 4 To 16 Line Decoder Block
- Output LED Block
- P & N Terminal Block
  - Output : DC 5V
- AND Gate Block
- OR Gate Block
- EX-OR Gate Block
- R-S Flip-Flop Block
- J-K Flip-Flop Block
- 3 To 8 Decoder Block
- 4 Bit Binary Counter Block
- 3 State Inverter Block
- 3 State Buffer Block

Accessory

- Experiment Manual : 1Copy
- Test Lead (Banana Plug) : 1Unit
- Power Cable : 1EA
- Dust Cover : 1EA

* Product's design and appearance can be changed without any notice.